

F14.3

水中アドワス	1		•			•			
	ເນ	Be	B 2.1	B 2.1	B 3.1				
	4	Въ	B Lo	B 20	B 10				
	က	້ . ອ	ე 	G 2.1	G 31 s				
	7	ိ့	ိ- ၅	ۍ د ي	ა <u>.</u> ი		•		
	τi	R 0.1	R 1.1	R 21	R 1.1				
	0	R 00	R 10	R 20	R 30				
	•	0	開	回 2	7	*	· 	\rightarrow	,

•

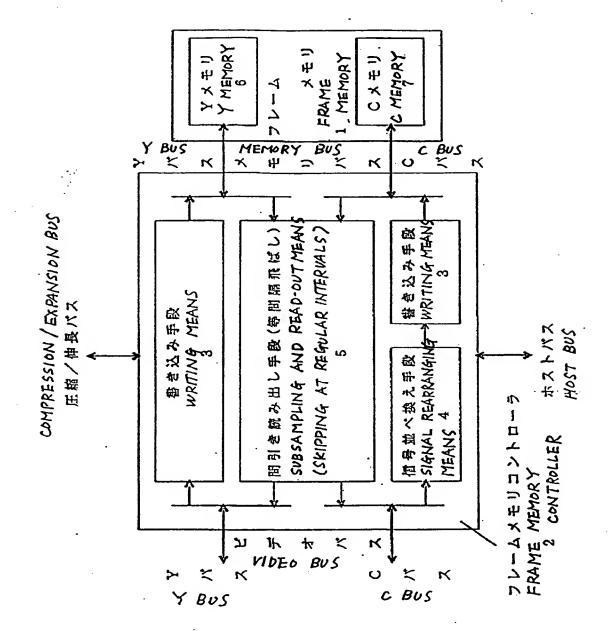
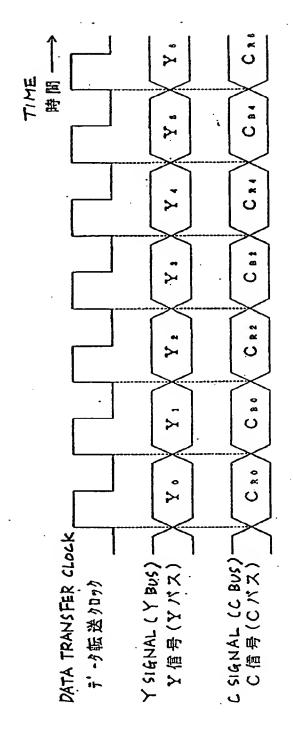


FIG. 5

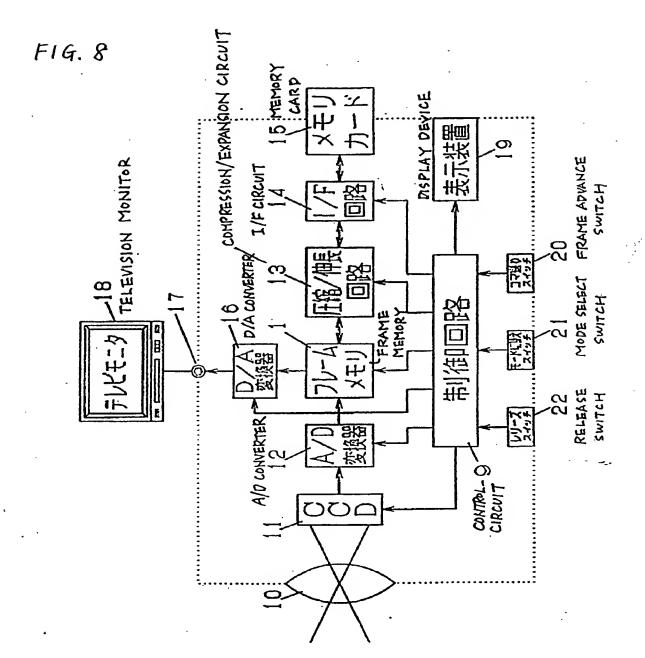
HORIZONTAL ADDRESS 水中アドレス	1		·				•	•
	~	O 8 8	O R 1 8	C 8 2 8	CRIS	_		
	ထ	C S	C B16 C.R14	C % 2.4	C R 14 C R 16			
	ω	° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° °	S E	ر اد اد	C 83.6			
	4	C B 0 4	C 814	C 8 2 4	C 334			
	တ	C Ro 2	C 211	C * 2 2	C R 3 2			
	7	C Roo	CRIO	C 2 2 0	C R 10			
	٦	C Boz	C 311	C 8 2 2	C 3 2 2			
	0	CBo	C 310	C B 2 0	C 830	•		
		0	щ.	Ø.	က		.•	>
			W	画	P	2 _	ج	ൂ.
			VER	TICAL	. AD	DRES	5	

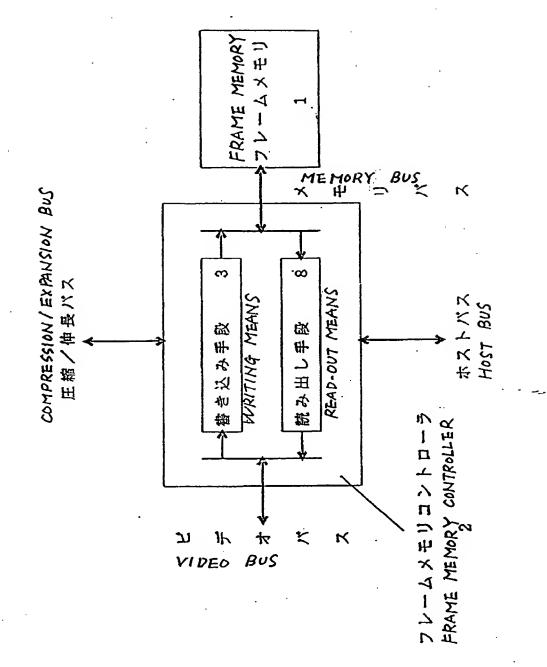
C。及びC。個号の格徴アドレス(Cメモリ) Storafe Address of Ce and Cr Signal (CMEMORY)

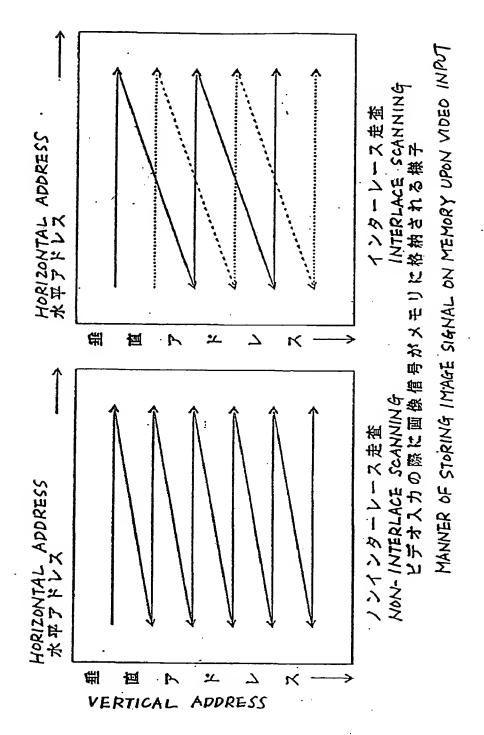


HORIZONTAL ADDRESS 木中アドレス FIG. 7 9 C * 2 \$ Cass ယ C # 14 C # 2.1 CRIT C 8 2 2 ന CB30 CBS Ø CREE C R 3 2 河 K

Ca及びCa伯母の格徴アドレス(Cメモリ) Stokage Address of Ca And Cr Signal (Chemory)

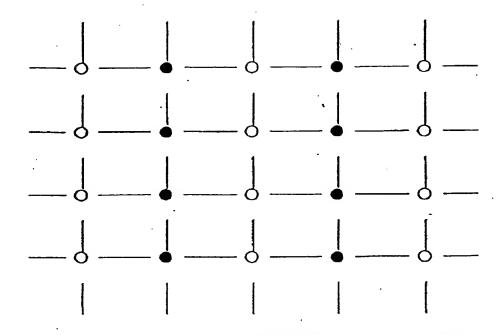






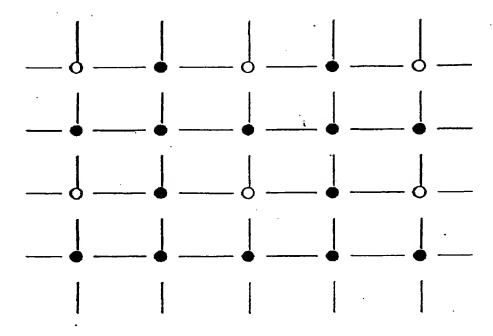
	T -		·	,				
G	В	G	В	G	В	G	В	G
R	G	R	G	R	Ģ	R	G	R
G.	В	G	В	Ģ	В	G	В	G
R	G	·R	Ġ	R	G	R	G	R
G	В	G	В	G	В	G	В	G
R	G	R	G	R	G	R	G	R
G	В	G	В	G	В	G	В	G
R	G	Ŕ	G	R	G	R	G	R

ベイヤー配列の単板カラーフィルタ SINGLE DISC COLOR FILTER HAVING BAYER ARRANGEMENT

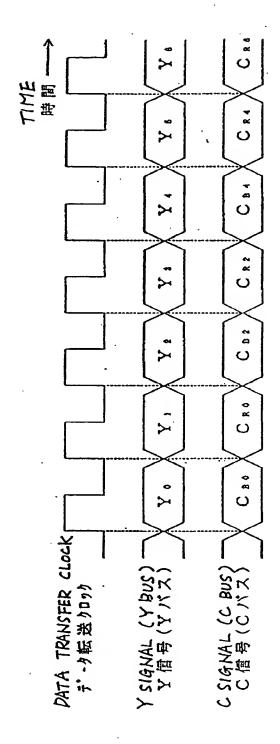


YC.C.且つ4:2:2の画像信号のサンプリング座標
SAMPLING COORDINATES OF YCBCR 4:2:2
IMAGE SIGNAL

F19.13



Y C • C • 且つ4:1:1の画像信号のサンプリング座標 SAMPLING COORDINATES OF YCB CR 4:1:1 IMAGE SIGNAL



ķ.

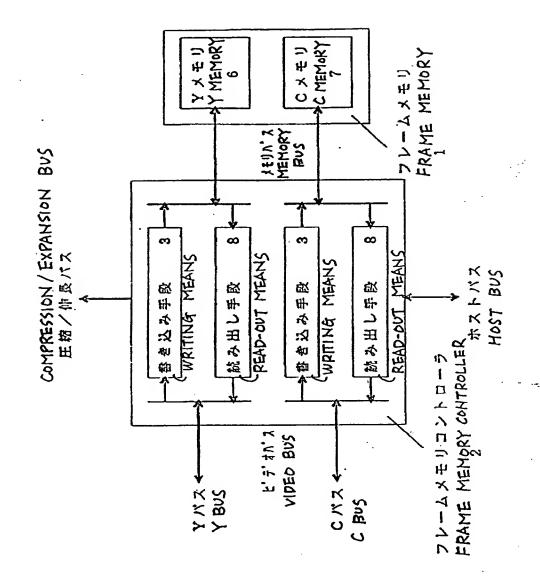


FIG. 16 (a)

ス

HORIZONTAL ADDRESS 水平アドレス

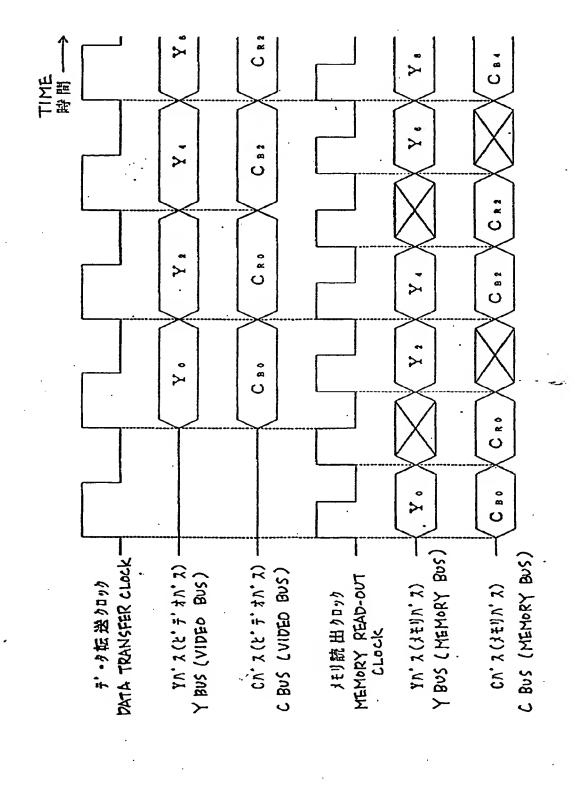
			0	1	2	3	4	5	•	n		•	
VERTICAL ADDRESS		0	Y	Υ	Y	Y . 2	Y • 4	Y		You			
	Æ	1	Y 10	Y	Y 4.2	Y	Y 14	Y 14		Y			
	道	2	Y 20	Y 2.	Y 22	Y 23	Y	Y 26	•	Y 24	•	•	
	ァ	3	Y	Y 3.1	Y	Y	Y 3.4	Y		Y 2 a			
	۴	•		<u> </u>		•	_L		•	•			
	V	m	Y	Yat	Y	Y	Yas	Y	T -	Y			
	ス	1	•										

Y信号の格納アドレス (Yメモリ). STORAGE ADDRESS OF Y SIGNAL (Y MEMORY)

HORIZONTAL ADDRESS FIG. 16 (b) 水平アドレス n + 15 2 3 1 Czen C RO4 C ... C ... C . . . C B a 4 CRL VERTICAL ADDRESS C = 1 2 CRIZ CR14 CRIO 垂 1 C x 2 4 CRZ C x z z 直 2 C . 2 0 C 820 C . . 7 3 ĸ

> この場合nは偶数 INTHIS CASE, れIS INTE年R

C *及びC *信号の格納アドレス(C メモリ) STORAGE APPRESS OF CB AND CR SIGNAL (C MEMORY)



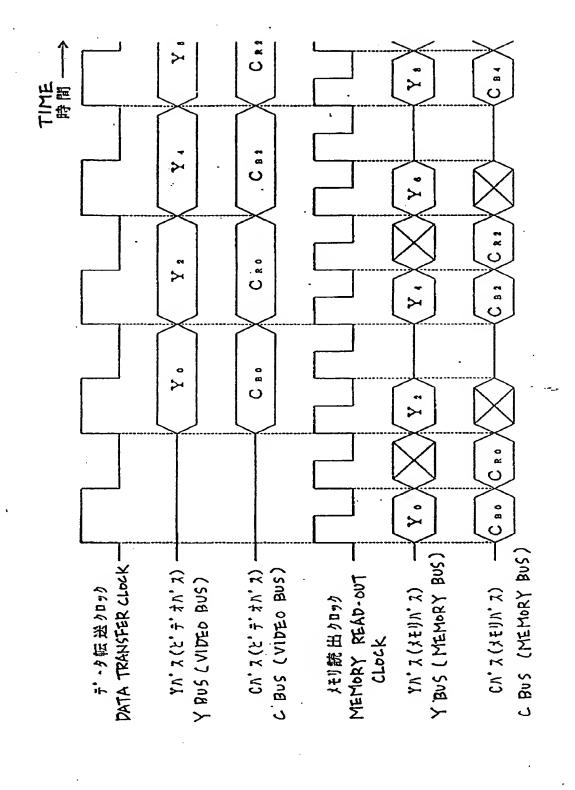


Fig. 19

